	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	22	(second adj patch) with slot	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/27 11:04	
2	BRS	L3	19	2 and slot$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/27 11:05	
3	BRS	L4	3	3 and ground$3 and (microstrip adj line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/27 11:06	
4	BRS	L2	46	stacked adj patch adj antenna	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/27 11:09	
5	IS&R	L5	1716	(343/700ms).CCLS.	USPAT	2004/01/27 11:21	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	6	(microstrip adj line) with (tuning adj stub)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/27 13:05	
2	BRS	L2	0	(microstripline) with (tuning adj stub)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/27 13:05	
3	BRS	L3	26	(microstrip adj line) and (tuning adj stub)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/27 13:09	
4	BRS	L4	6	3 and (patch adj antenna)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/27 13:10	